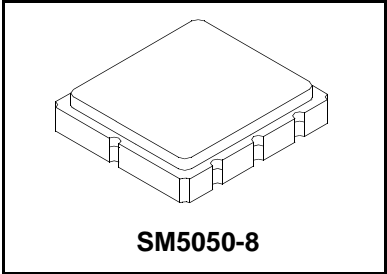




SF2006C

**190.00 MHz
SAW Filter**



- **SAW Filter for W-CDMA**
- **5.0 X 5.0 X 1.7 mm Surface-Mount Case**
- **Complies with Directive 2002/95/EC (RoHS)**

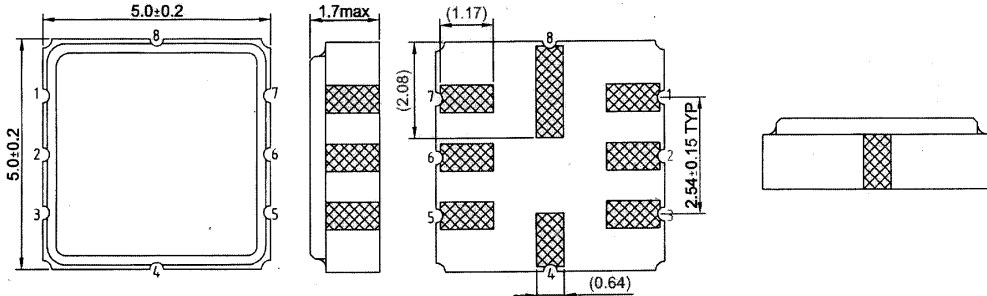
Maximum Rating

Rating	Value	Units
Input Power Level	10	dBm
Operating Temperature	-30 to +70	°C
Storage Temperature	-30 to +70	°C

Electrical Characteristics

Characteristic	Sym	Notes	Min	Typ	Max	Units
Center Frequency	f_c			190		MHz
Insertion Loss	IL			8.0	10	dB
Passband width	BW		4.8	5.73		MHz
Group Delay				70	150	ns
Phase Linearity in $f_c \pm 2$ MHz (RMS)				6	8	Deg
Attenuation: (Reference level from Min IL)						
Fc -20 to -10 MHz (170 -180 MHz)			27	34		dB
Fc -10 to -4.5 MHz (180 -185.5 MHz)			25	31		
Fc +4.5 to +10 MHz (194.5 -200 MHz)			25	30		
Fc +10 to +20 MHz (200 -210 MHz)			27	33		
Ultimate Rejection				50		
Lid Symbolization (Y=year, WW=week, S=shift)			RFM 734 YWWS			

Outline Drawing

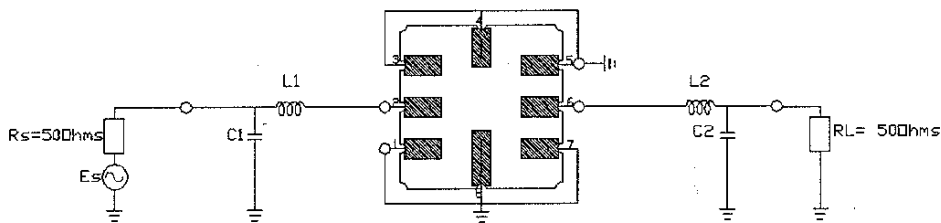


- Pin2: Input RF
- Pin6: Output RF
- Pin1: Input Ground
- Pin5: Output Ground
- Pin3, 4, 7, 8: Ground

Notes:

1. Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.
2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, f_c .
3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
4. "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
5. The design, manufacturing process, and specifications of this filter are subject to change.
6. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
7. US and international patents may apply.
8. RFM, stylized RFM logo, and RF Monolithics, Inc. are registered trademarks of RF Monolithics, Inc.
9. ©Copyright 1999, RF Monolithics Inc.
10. Electrostatic Sensitive Device. Observe precautions for handling

D. MATCHING CONFIGURATION:



C1=10pF C2=20pF

L1=65nH L2=80nH

Note1: The values of components for matching circuit will vary slightly due to parasitic capacitor of PCB

E. FREQUENCY CHARACTERISTICS:

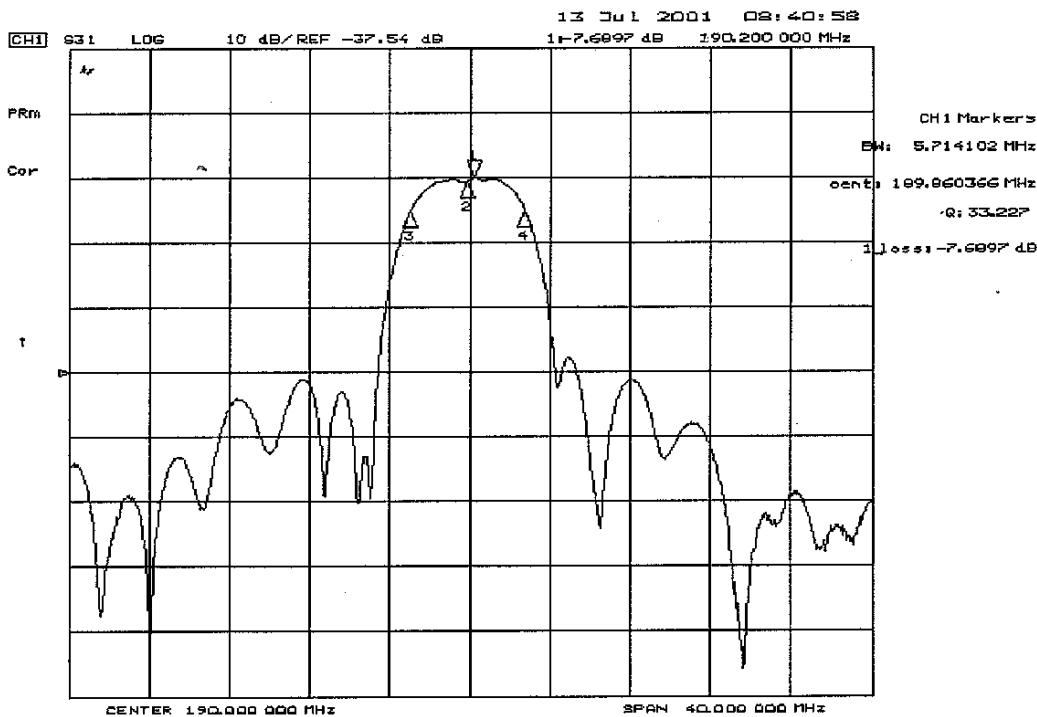
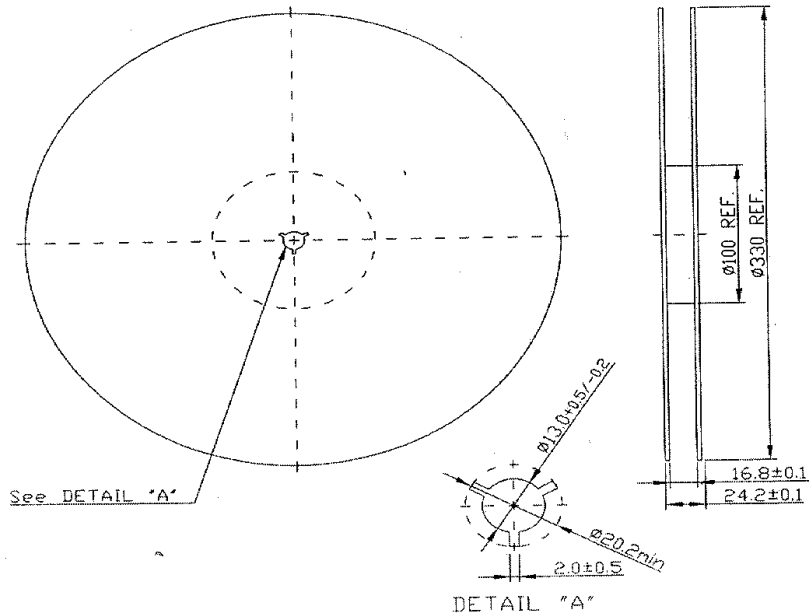


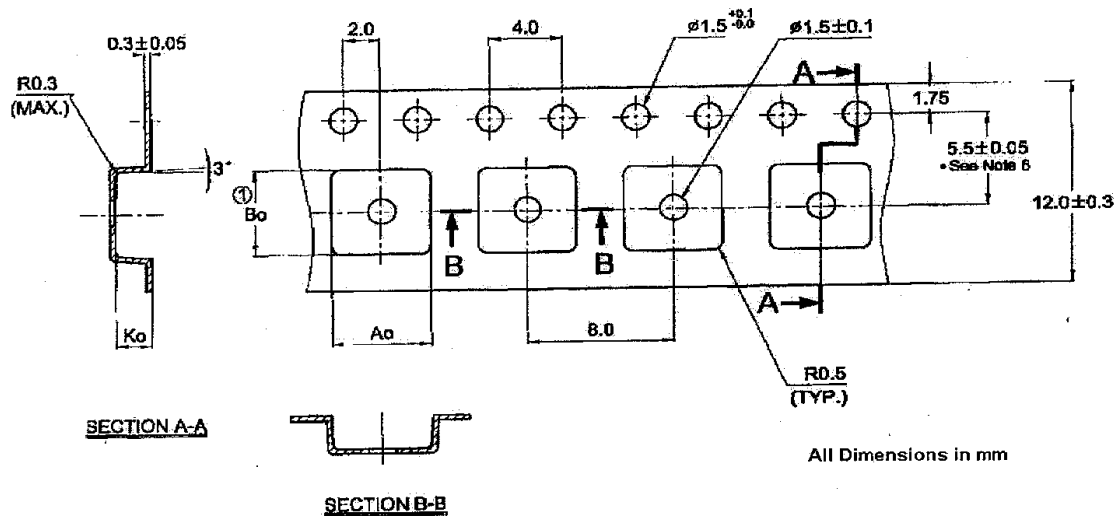
Fig-1 S21 Response

F. PACKING:

1. REEL DIMENSION



2. TAPE DIMENSION



$Ao = 5.30 \pm 1$ mm
 $Bo = 5.30 + 0.2 / - 0.0$ mm
 $Ko = 2.0 \pm 0.1$ mm